

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kamiko, *et al.* Docket No.: INF 2004 LW 2488 US
Serial No.: 10/523,517 Art Unit: 2186
Filed: February 4, 2005 Examiner: Alsip, Michael
For: Methods for Operating a CPU having an Internal Data Cache

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08A that may be considered material to the examination of the above-identified application.

Applicants submit the present Information Disclosure Statement (IDS) to correct an error in the form PTO-892 attached to the March 28, 2007 Office Action. The Examiner cited U.S. Patent No. 6,401,199 B1 in the Office Action dated March 28, 2007; however, in the attached form PTO-892, the Examiner incorrectly listed U.S. Patent No. 5,401,199. Applicants submit this IDS to clarify the record for the above-referenced application.

No fee is believed due in connection with this filing. However, in the event that there are any fees due, please charge the same, or credit any overpayment, to Deposit Account No. 50-1065.

Respectfully submitted,

October 1, 2007
Date

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